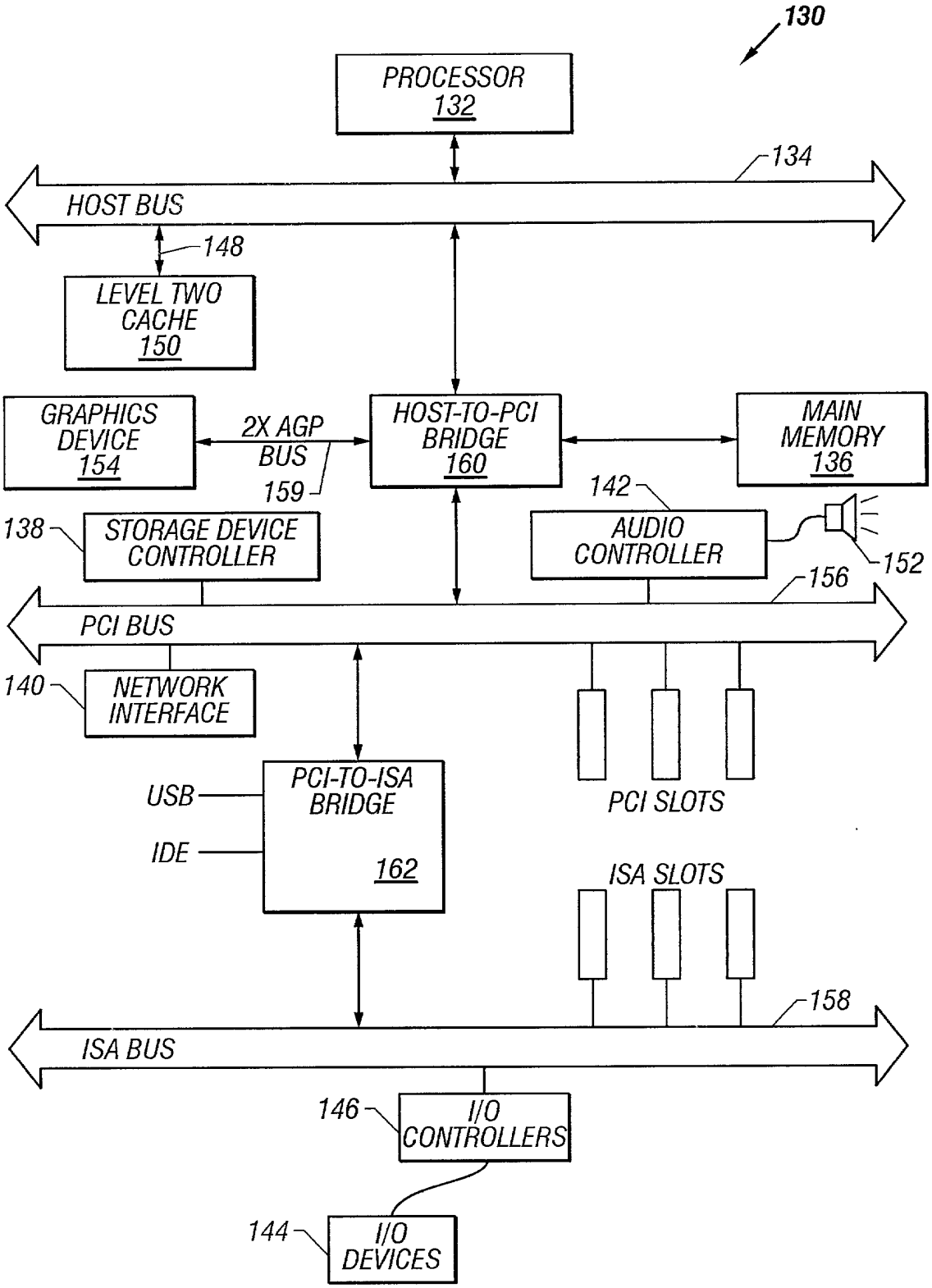
130
PROCESSOR
132
134
HOST BUS
148
LEVEL TWO
CACHE
150
GRAPHICS
DEVICE
154
2X AGP
BUS
159
HOST-TO-PCI
BRIDGE
160
MAIN
MEMORY
136
142
138
STORAGE DEVICE
CONTROLLER
AUDIO
CONTROLLER
152
156
PCI BUS
140
NETWORK
INTERFACE
USB
IDE
PCI-TO-ISA
BRIDGE
162
PCI SLOTS
ISA SLOTS
158
ISA BUS
146
I/O
CONTROLLERS
144
I/O
DEVICES

FIG. 1

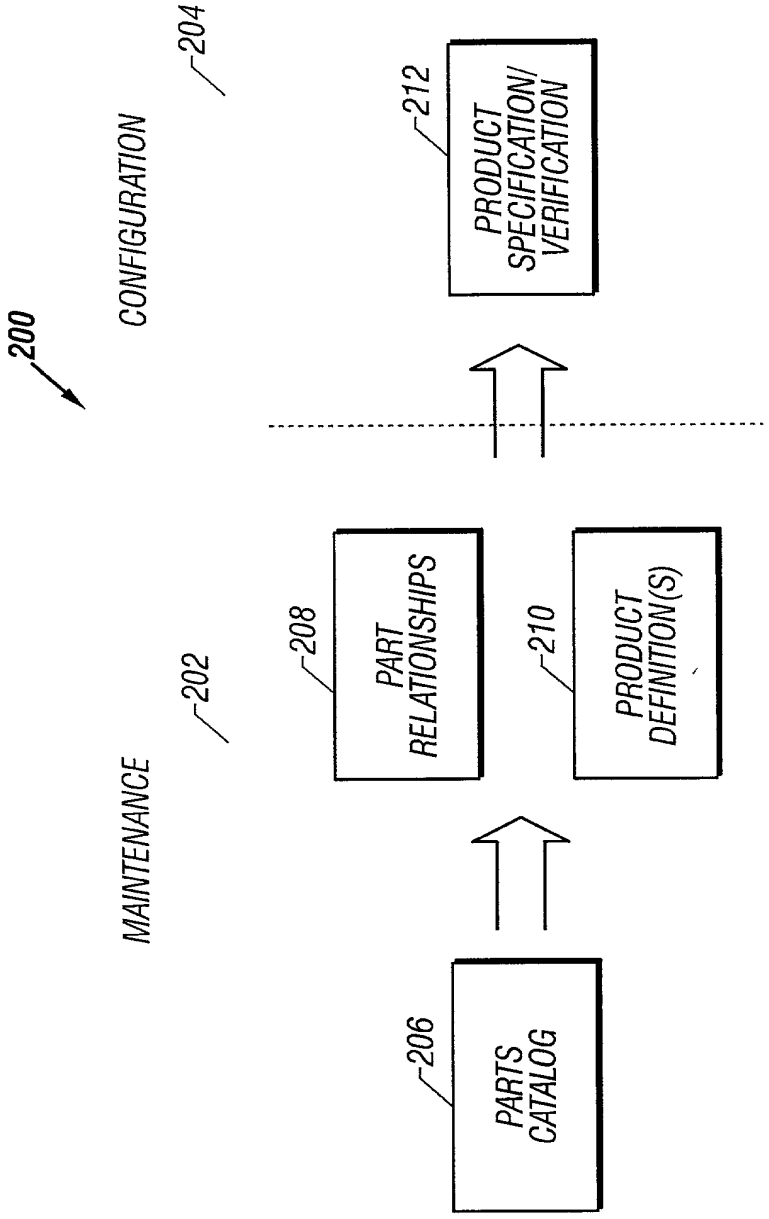
200
CONFIGURATION
204
MAINTENANCE
202
208
PART RELATIONSHIPS
210
PRODUCT DEFINITION(S)
212
PRODUCT SPECIFICATION/ VERIFICATION
206
PARTS CATALOG

FIG. 2

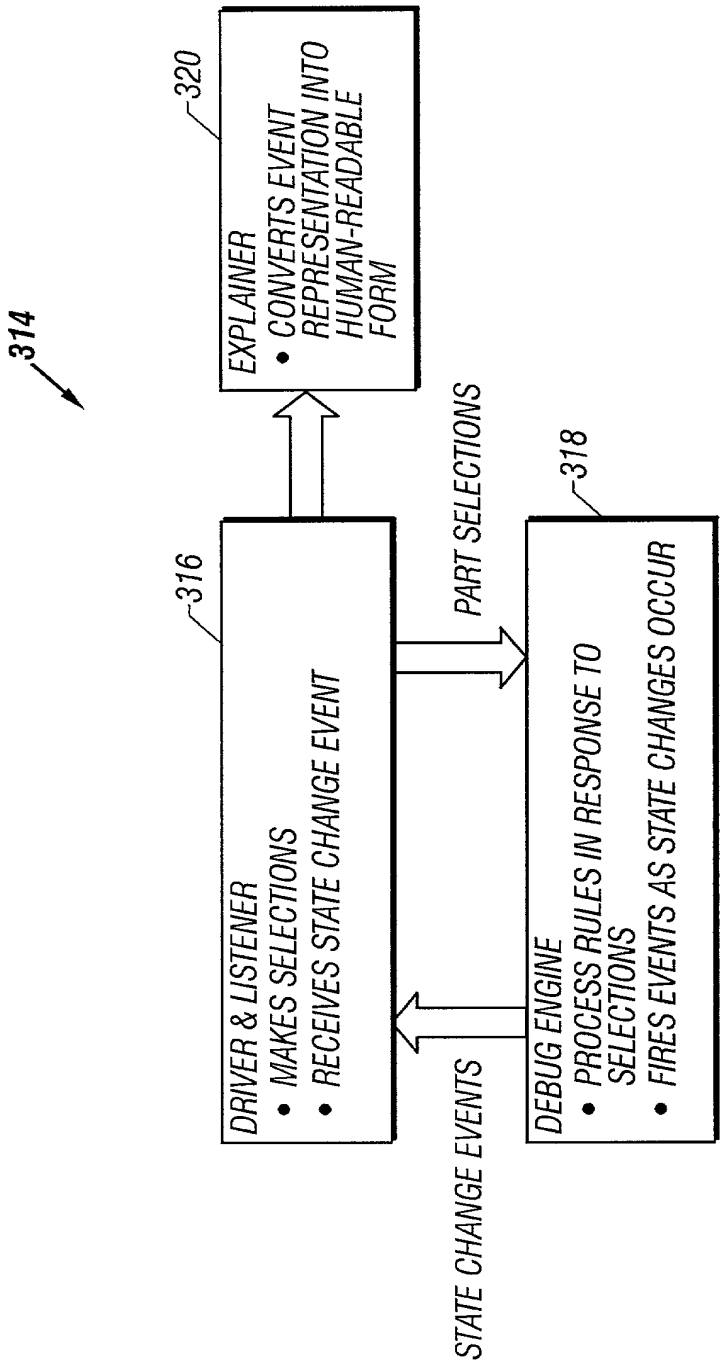
314
320
EXPLAINER
• CONVERTS EVENT REPRESENTATION INTO HUMAN-READABLE FORM
316
DRIVER & LISTENER
• MAKES SELECTIONS
• RECEIVES STATE CHANGE EVENT
PART SELECTIONS
318
DEBUG ENGINE
• PROCESS RULES IN RESPONSE TO SELECTIONS
• FIRES EVENTS AS STATE CHANGES OCCUR
STATE CHANGE EVENTS

FIG. 3A

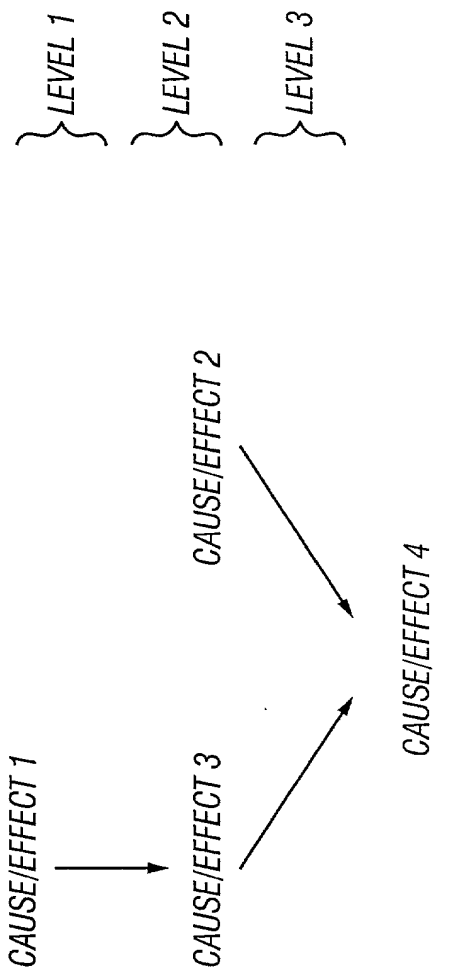

FIG. 3B

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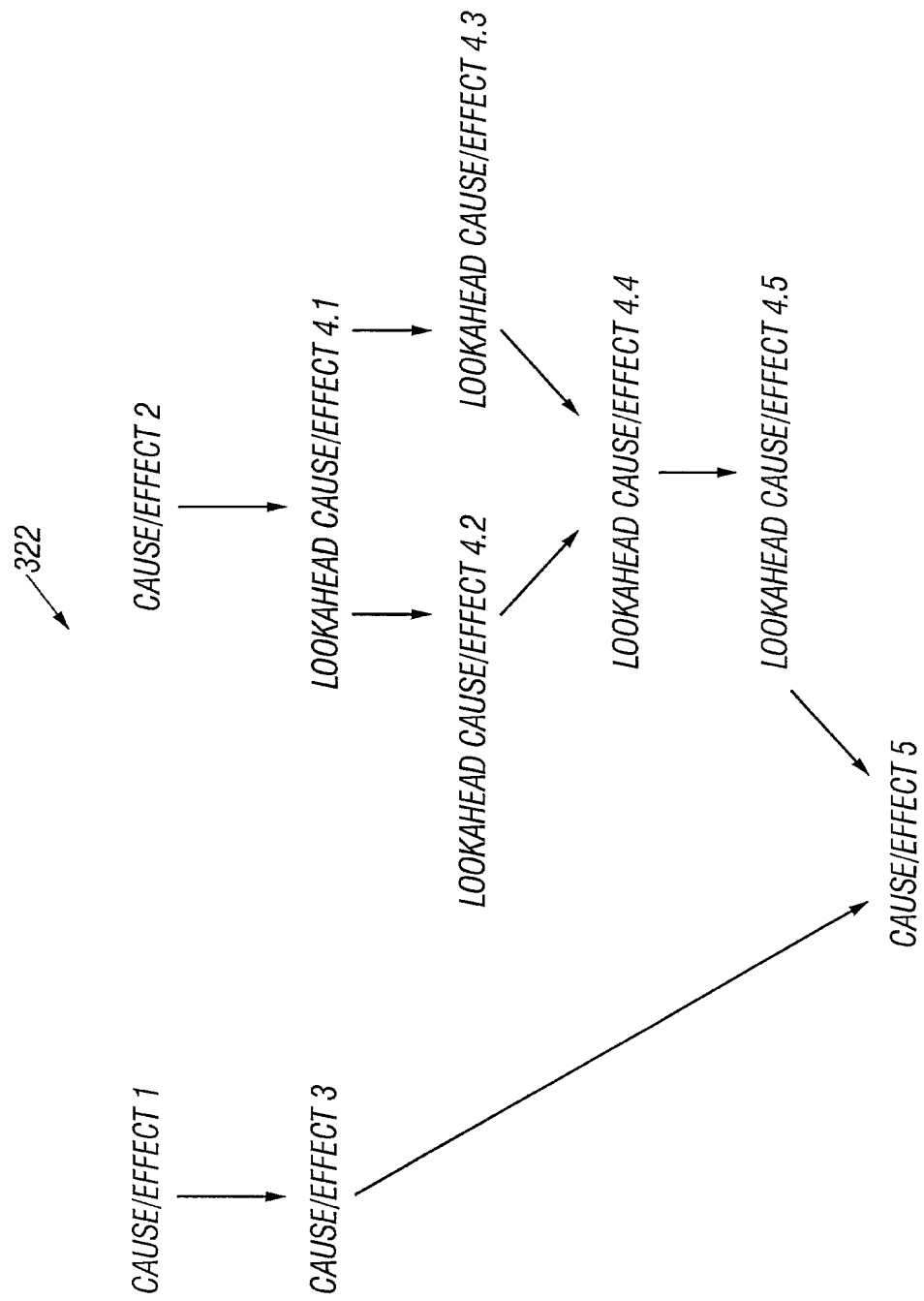
322
CAUSE/EFFECT 1
CAUSE/EFFECT 2
CAUSE/EFFECT 3
LOOKAHEAD CAUSE/EFFECT 4.1
LOOKAHEAD CAUSE/EFFECT 4.2
LOOKAHEAD CAUSE/EFFECT 4.3
LOOKAHEAD CAUSE/EFFECT 4.4
LOOKAHEAD CAUSE/EFFECT 4.5
CAUSE/EFFECT 5

FIG. 3C

324
LOOKAHEAD CAUSE/EFFECT 4.1
LOOKAHEAD CAUSE/EFFECT 4.2
LOOKAHEAD CAUSE/EFFECT 4.3
LOOKAHEAD CAUSE/EFFECT 4.4
LOOKAHEAD CAUSE/EFFECT 4.5
326
CAUSE/EFFECT 1
CAUSE/EFFECT 2
CAUSE/EFFECT 3
LOOKAHEAD CAUSE/EFFECT 4
CAUSE/EFFECT 5

FIG. 3D